

FORM PTO 1449 (modified)

ATTY DOCKET NO.
2005_0621ASERIAL NO.
NEW

101532679

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEAPPLICANT
Tor Sverre LANDELIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)FILING DATE
April 26, 2005

GROUP 2814

Date Submitted to PTO: April 26, 2005

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
mbh	AA	6,509,606	1/2003	Merrill et al.			
mbh	AB	2001/0045595	11/2001	Black et al.			
mbh	AC	4,538,287	8/1985	Roberts et al.			
mbh	AD	5,336,937	8/1994	Sridhar et al.			
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
mbh	AJ	95/22201	8/1995	WO			Abstract
mbh	AK	4-118965	4/1992	JP			Abstract
mbh	AL	2000-298995	10/2000	JP			Abstract
	AM						
	AN						

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

mbh	AO	O. Naess et al., "Tunable floating-gate low-voltage transconductor," 2002 IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS. PROCEEDINGS (CAT. NO. 02CH37353), 22002 IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, Phoenix-Scottsdale, AZ, USA, May 26-29, 2002, vol. 4, 2002, pp. IV-663 - IV-666, XP002272572, Piscataway, NJ, USA, IEEE, USA ISBN: 0-7803-7448-7
	AP	

EXAMINER *Muradlin bin Hafez*DATE CONSIDERED *12/27/05*